IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned Examiner: To Be Assigned

In re DIVISIONAL PATENT APPLICATION of

Applicant(s) : Junichi HIKITA et al)
Appln. No. : To Be Assigned)

Filed : Herewith) INFORMATION) DISCLOSURE) STATEMENT

For : PRODUCTION PROCESS FOR)
SEMICONDUCTOR DEVICE)

Atty. Dkt. : AI 258 D1)_____

Mail Stop: Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

The Examiner is respectfully requested to consult related copending application serial number 09/511,800, filed on February 23, 2000, for which priority under 35 U.S.C. §120 has been relied upon, for copies of the references listed on the attached PTO-892 forms which were cited by the Examiner in the parent application.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the document submitted. Consideration of the previously submitted documents is respectfully requested.

Respectfully submitted,

December 31, 2003

Date

Steven M. Rabin (Reg. No. 29,102)

Customer No. 23995

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